

# FIGURE 1

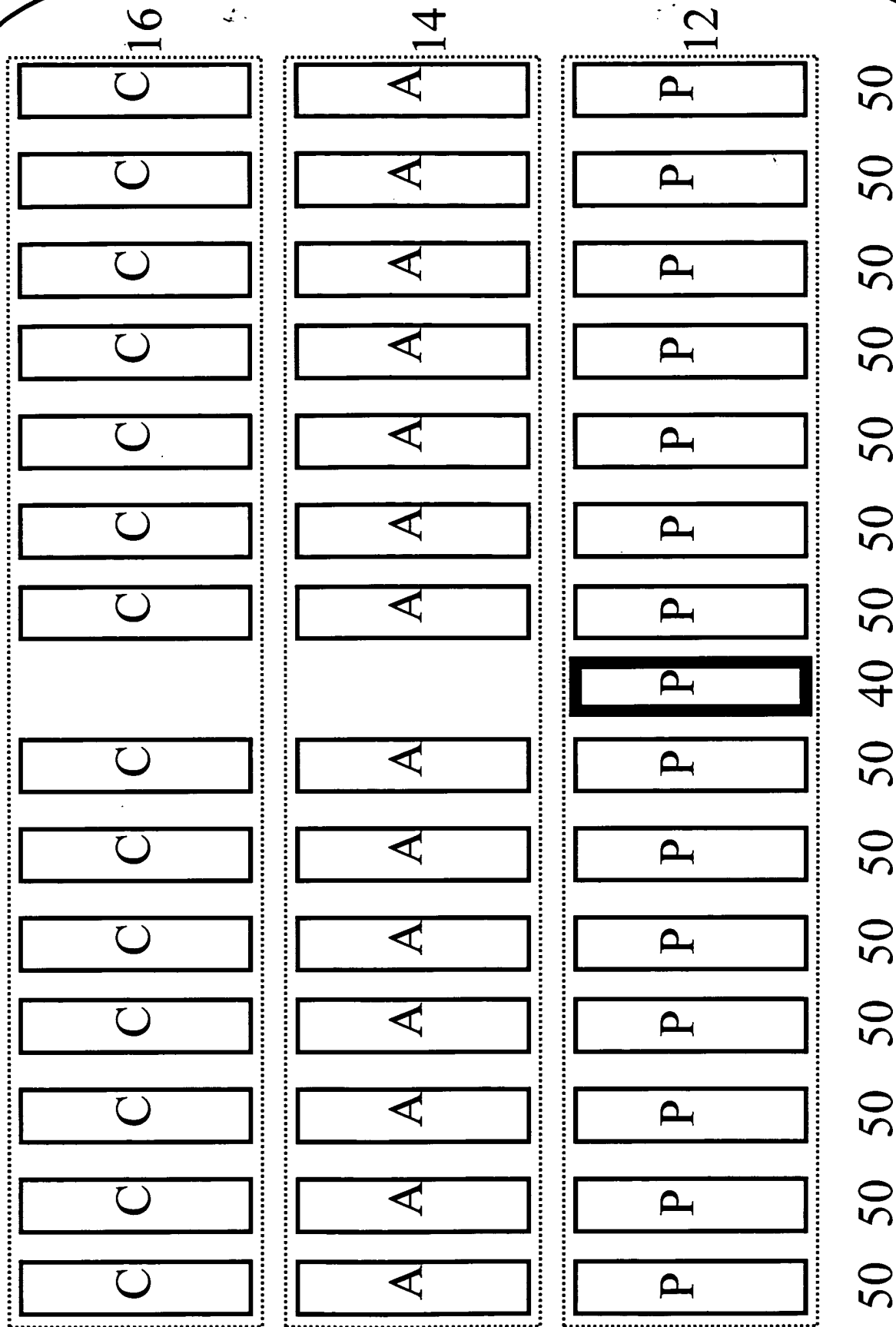
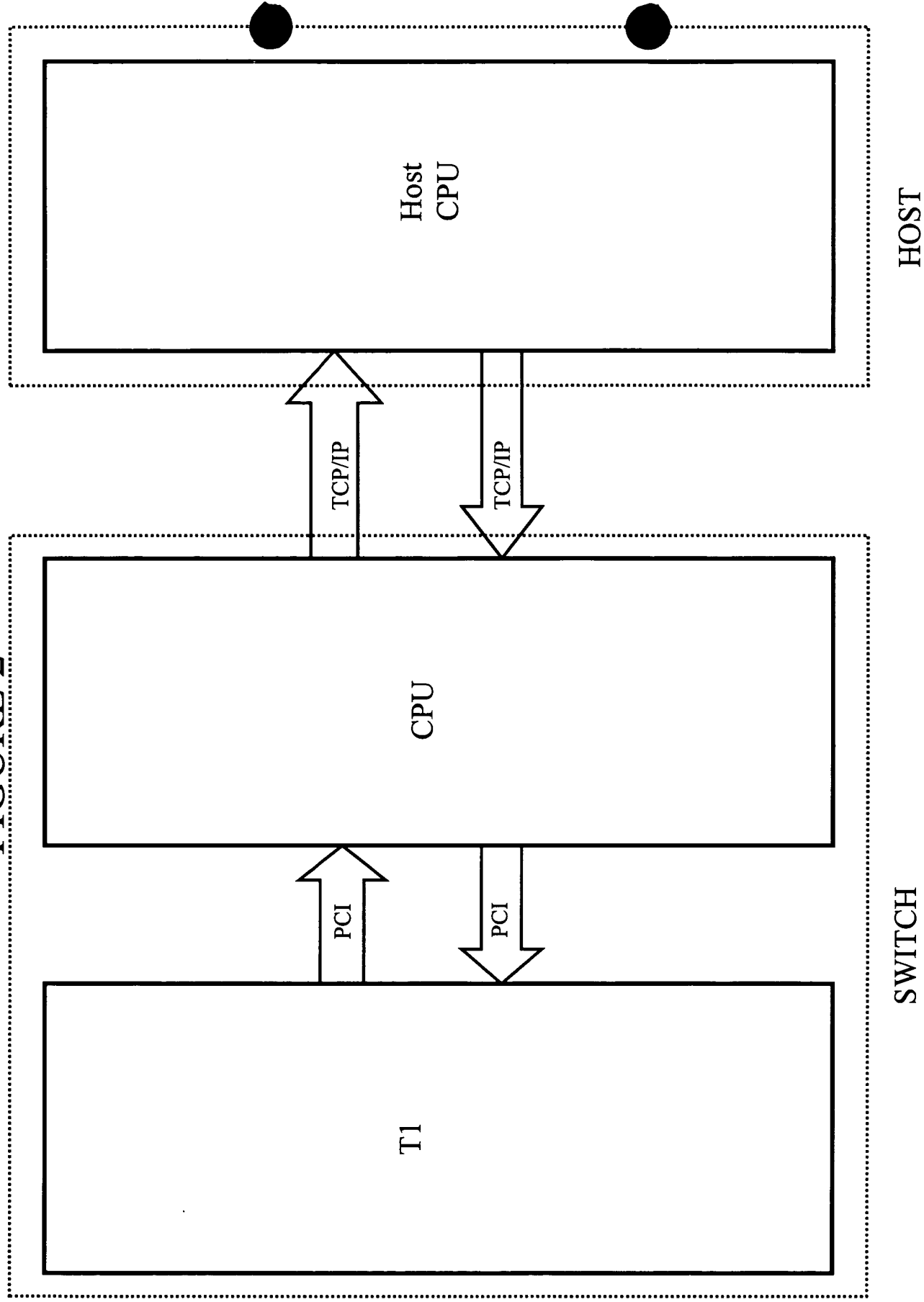


FIG. 2 is a block diagram of a system architecture showing a Host CPU connected to a SWITCH via a CPU and T1 components. The Host CPU is connected to the CPU via TCP/IP, and the CPU is connected to T1 via PCI.

FIGURE 2



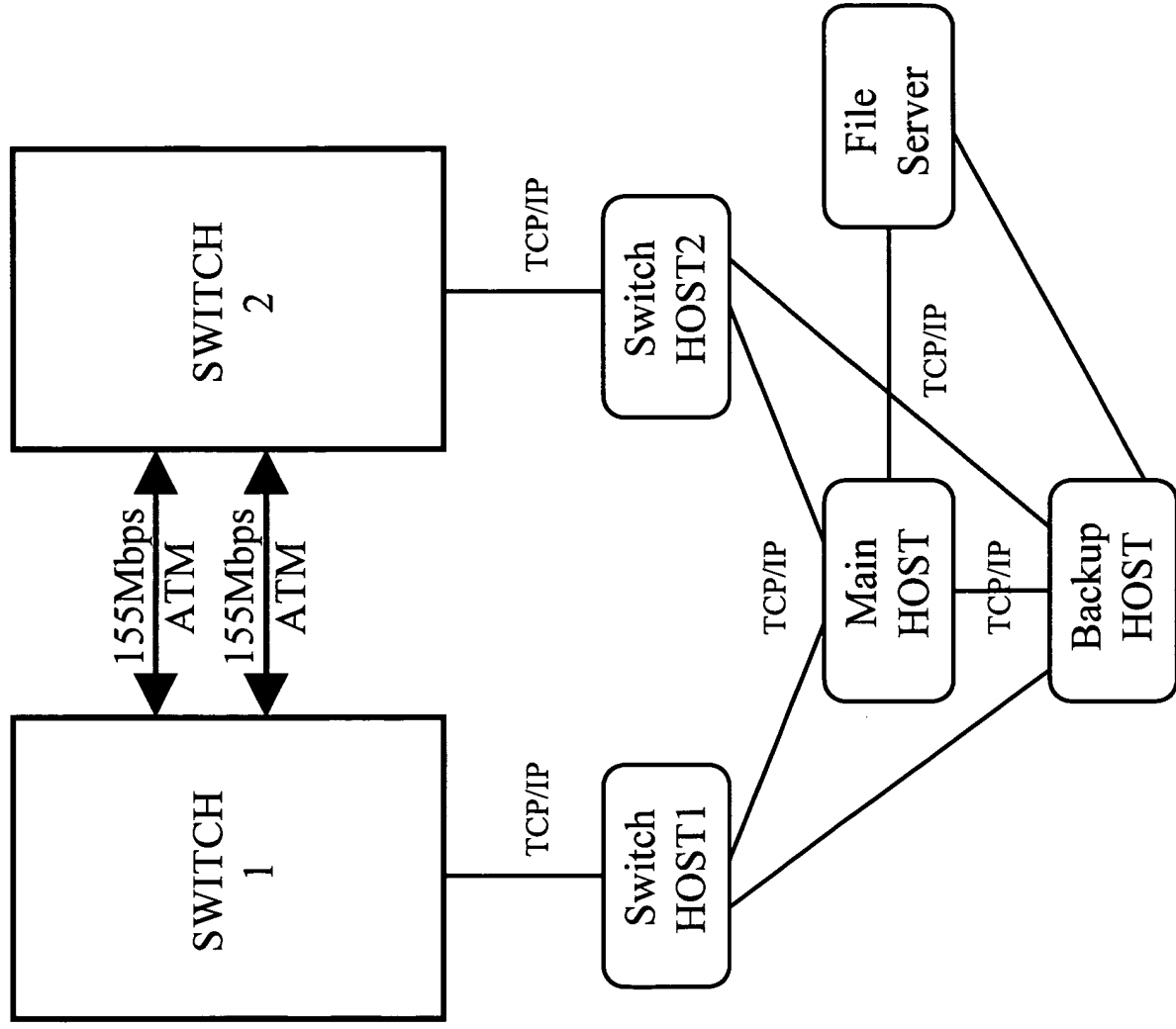
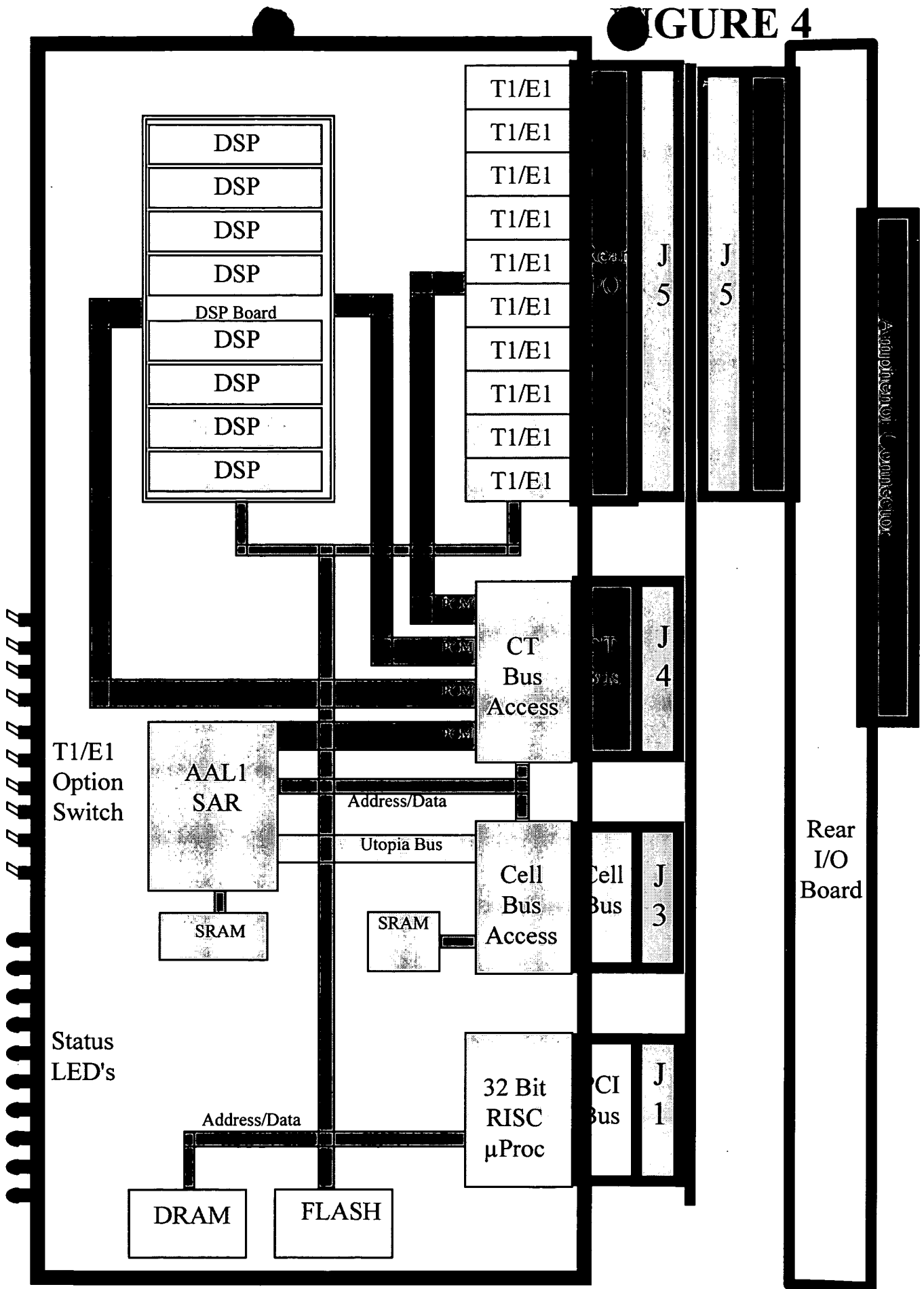


FIGURE 3

FIGURE 4



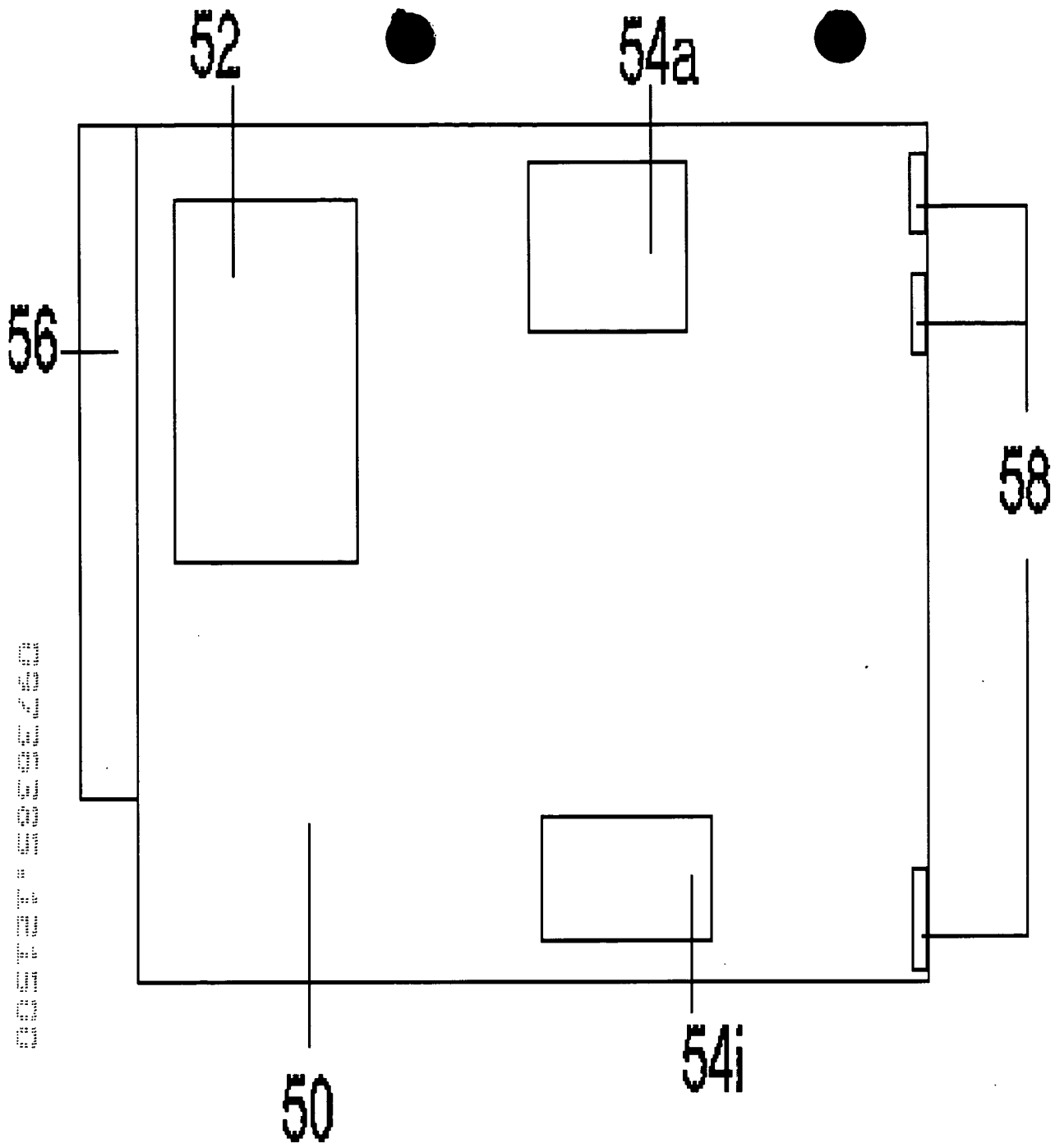


FIG. 5